

Docket No. 57341-PCT / 45107

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT: KRAUTSCHNEIDER, Wolfgang et al.

U.S.S.N.: N/A

Art Unit: N/A

FILED: HEREWITH

Examiner: N/A

FOR: ARRANGEMENT WITH IMAGE SENSORS

\*\*\*\*\*

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service on this date **MARCH 29, 2002**, in an envelope as "Express Mail Post Office to Addressee," mailing Label No.: **EL 932681199 US** addressed to: **BOX PCT Commissioner for Patents, Washington, D.C. 20231.**

By:

*Judith A. Herrick*  
JUDITH A. HERRICK

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**INFORMATION DISCLOSURE STATEMENT  
(SUBMISSION AFTER FILING OF AN APPLICATION  
BUT BEFORE FINAL REJECTION OR NOTICE OF ALLOWANCE)**

Commissioner for Patents  
Washington, D.C. 20231

Date: MARCH 29, 2002

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, applicant(s) hereby submit(s) an Information Disclosure Statement for consideration by the Examiner.

**I. LIST OF PATENTS, PUBLICATIONS OR OTHER INFORMATION**

The patents, publications or other information submitted for consideration by the Office are listed on PTO-1449, attached hereto.

**II. COPIES**

- a. X Submitted herewith is a legible copy of (i) each U.S and foreign patent; (ii) each publication or that portion which caused it to be listed; and (iii) all other information or that portion which caused it to be listed.

III. CONCISE EXPLANATION OF THE RELEVANCE  
(check at least one box)

- a. ☒ Except as may be indicated below in (b), all of the patents, publications or other information are in the English language or were cited in an English language Search Report, a copy of which is attached hereto (concise explanation not required).
- b. ☐ A concise explanation of the relevance of all patents, publications or other information listed that is not in the English language is as follows:
- c. ☐ The following additional information is provided for the Examiner's consideration:

**FEES**

IV. THIS IDS IS BEING FILED UNDER 37 C.F.R. § 1.97(b) (check one box)

- a. ☐ within three months of the filing date of a national application (37 C.F.R. § 1.97(b) (1)). No fee or certification is required.
- b. ☐ within three months of the date of entry of the national stage as set forth in §1.491 in an international application (37 C.F.R. § 1.97(b) (2)). No fee or certification is required.
- c. ☒ before the mailing date of a first Action on the merits (37 C.F.R. § 1.97(b) (3)). No fee or certification is required. In the event that a first Office Action on the merits has been issued, please consider this IDS under 37 C.F.R. § 1.97(c) and see the certification under 37 C.F.R. § 1.97(e) below, or, if no certification has been made, charge our deposit account a fee in the amount of \$230.00 as required by 37 C.F.R. § 1.17(p).

V. THIS IDS IS BEING FILED UNDER 37 C.F.R. § 1.97(c):  
(check one box)

before the mailing date of a Final Office Action under 37 C.F.R. § 1.113 (See 37 C.F.R. § 1.97(c) (1)) or before the mailing date of a Notice of Allowance under 37 C.F.R. § 1.311 (See 37 C.F.R. § 1.97(c) (2)).

- a. ☐ No certification; therefore, a fee in the amount of \$230.00 is required by 37 C.F.R. . § 1.17(p).
- or
- b. ☐ See the certification below. No fee is required.

VI. CERTIFICATION UNDER 37 C.F.R. § 1.97(e)  
(check only one box)

The undersigned hereby certifies that

- a. ☐ each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this IDS; or
- b. ☒ no item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application or, to the best of my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this statement.
- c. ☐ Some of the items of information were cited in a communication from a foreign Patent Office. As to this information, the undersigned certifies that each item of information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this IDS. As to the remaining information, the undersigned hereby certifies that no item of this remaining information contained in the IDS was cited in a communication from a foreign Patent Office in a counterpart foreign application or, to the best of my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this statement.

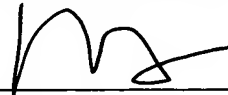
☐ Please charge Deposit Account No. 04-1105 in the amount of \$230.00 for the above-indicated fee. A triplicate copy of this paper is attached.

☒ No fee is required.

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If the Examiner has any questions concerning this IDS, he/she is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule (with a petition. If necessary) and charge the appropriate fee to Deposit Account No. 04-1105.

Respectfully submitted,



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<b>FORM PTO-1449</b>	DOCKET NO.: 57341-PCT (45107)	SERIAL NO.: N/A
<b>INFORMATION DISCLOSURE STATEMENT</b>	APPLICANT(S): KRAUTSCHNEIDER, Wolfgang et al.	
	FILING DATE: HEREWITH	GROUP NO.: N/A

**UNITED STATES PATENT DOCUMENTS**

EXAM. INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,025,146	06/18/1991	Kleinschmidt et al.	250	214
	AB	5,854,498	12/29/1998	Merrill	257	292
	AC	5,566,044	10/15/1996	Bergemont et al.	361	321.1
	AD	5,592,686	09/14/1999	Chou et al.	257	292
	AE	5,933,190	08/03/1999	Dierickx et al.	348	302

**FOREIGN PATENT DOCUMENTS**

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
BA	EPO 739 039	23.10.1996	EPO		YES
BB	WO 98/58411	23.12.1998	PCT		YES
BC	DE 198 25048	08.04.1999	GERMANY		YES

**OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)**

CA	R. Hauschild, et al., "A CMOS Optical Sensor System Performing Image Sampling on a Hexagonal Grid," <i>ESSCIRC 1996. Proceedings of the 22<sup>nd</sup> European Solid-State Circuits Conference, Proceedings of ESSCIRC 1996, Neuchatel, Switzerland, pages 304-307 1996;</i>
CB	Eric Y. Chou, et al., "Low Power Salient Integration Mode Image Sensor with a Low Voltage Mixed-Signal Readout Architecture," <i>Proceedings 1998. International Symposium on Low Power Electronics and Design, August 10-12, 1998;</i>
CC	Yoshinori Iida, et al., "A 1/4-Inch 330k Square Pixel Progressive Scan CMOS Active Pixel Image Sensor," <i>IEEE Journal of Solid-State Circuits, pages 2042-2047, 1998;</i>
CD	Eric R. Fossum, "CMOS Image Sensors: Electronic Camera On A Chip," <i>IEEE. pages 17-25, 1995.</i>

EXAMINER:

DATE: